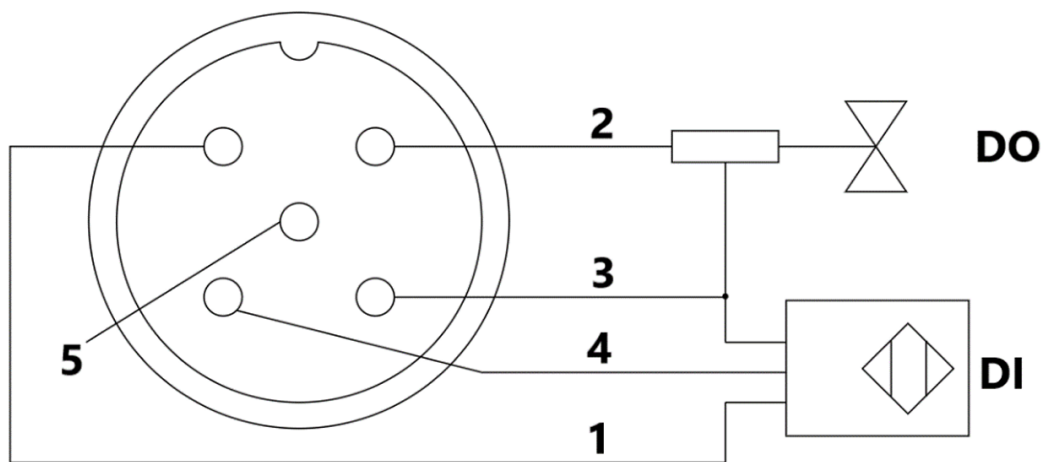
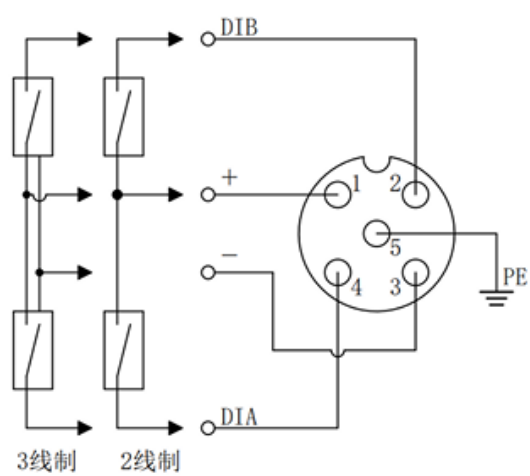
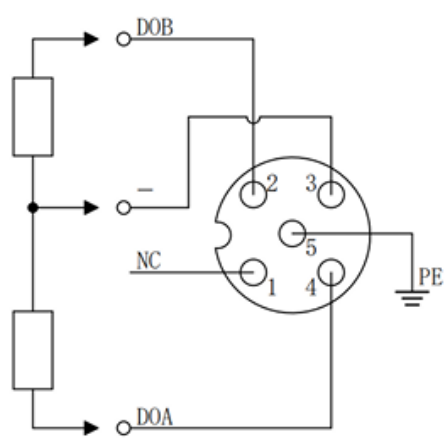


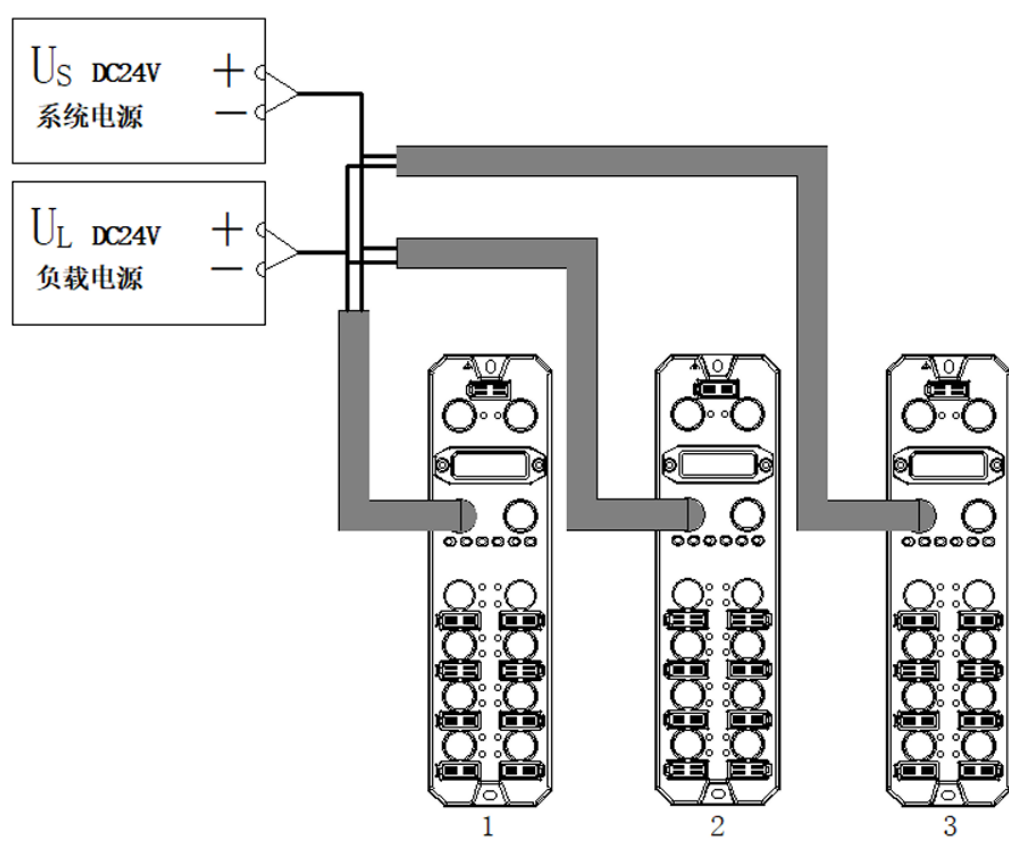


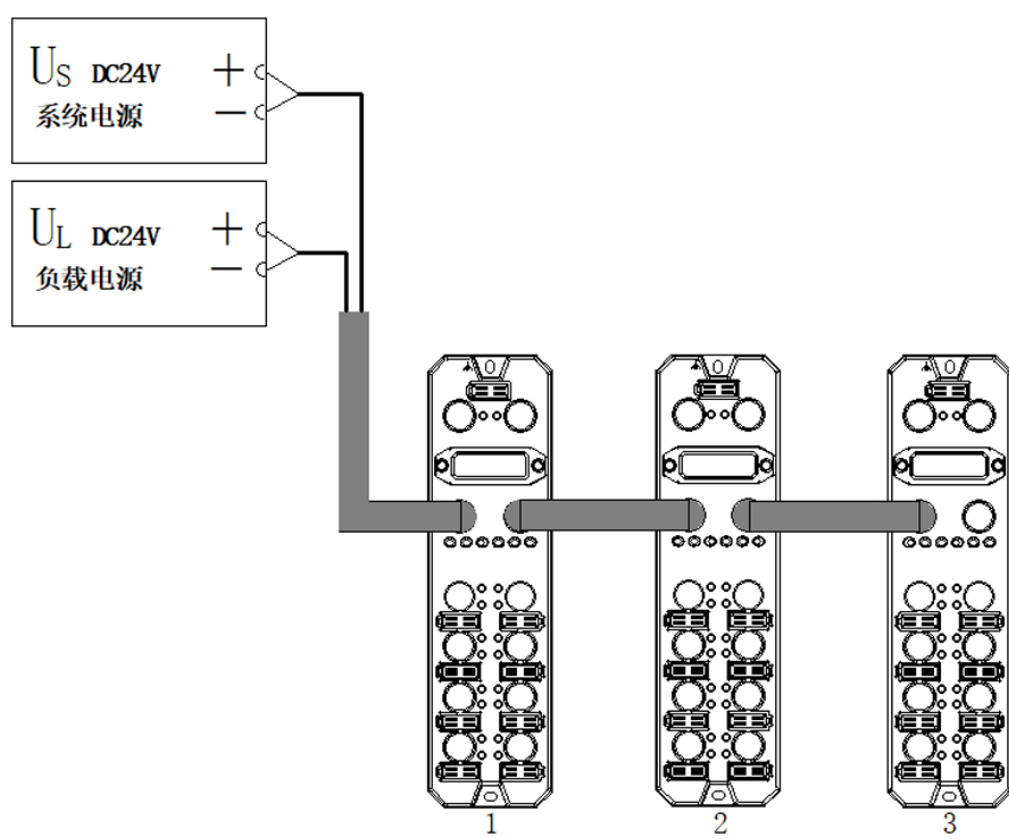
PNP输入

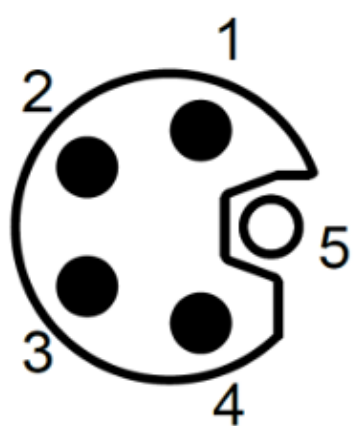


PNP输出

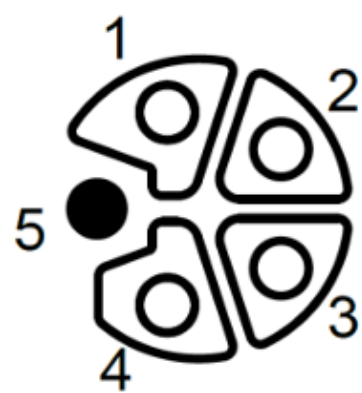






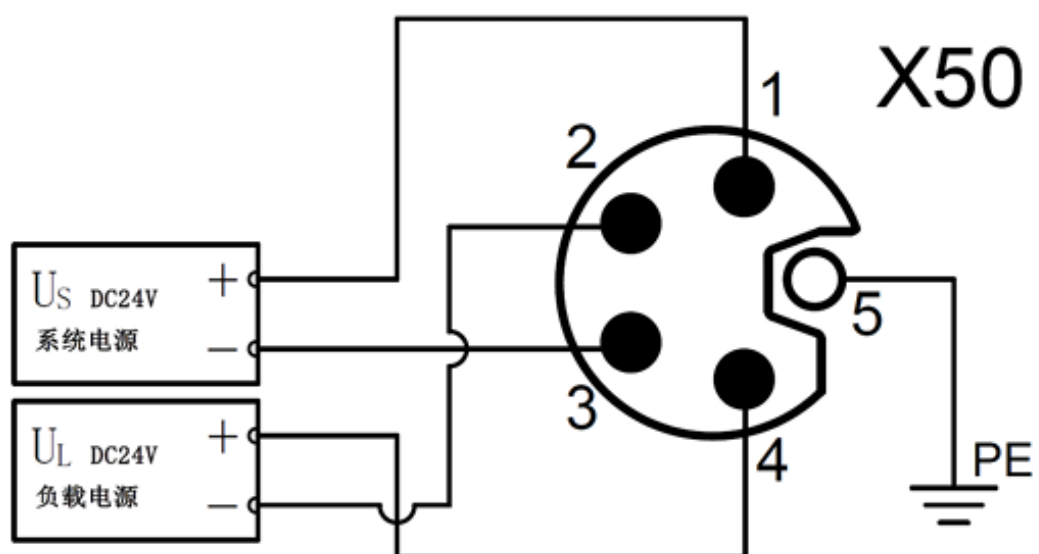


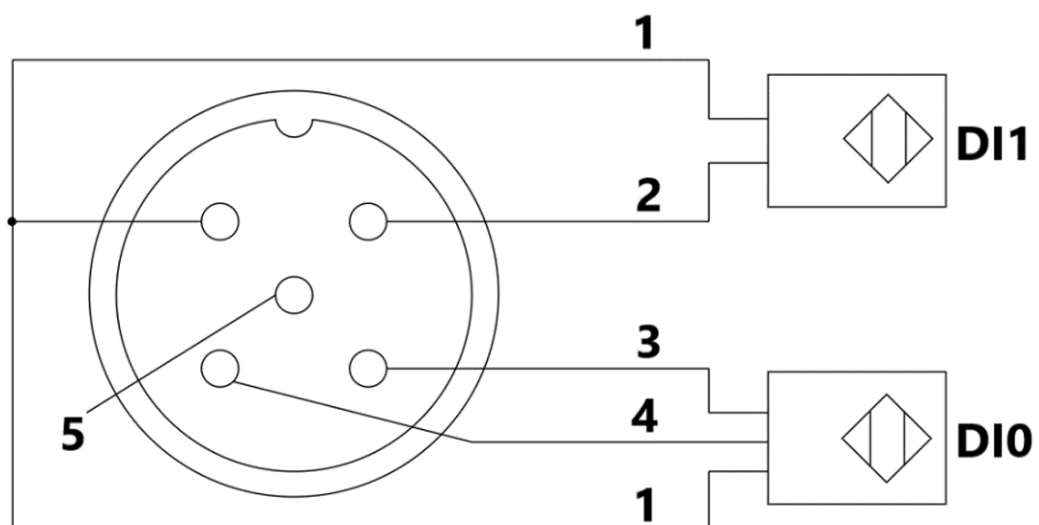
IN

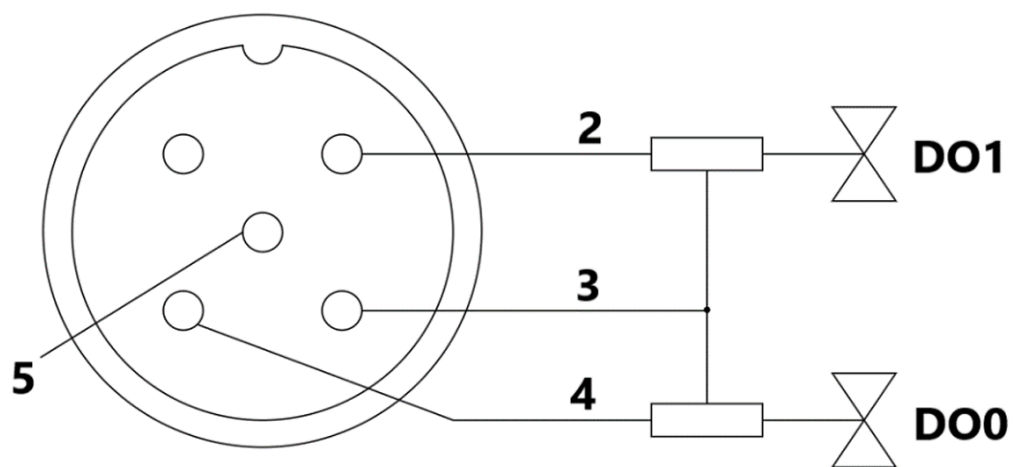


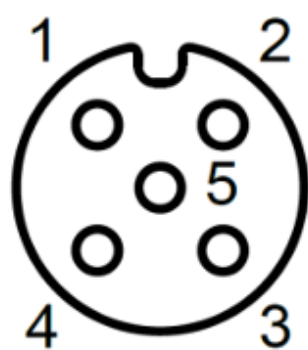
Power

OUT

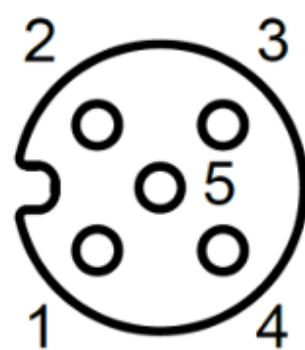




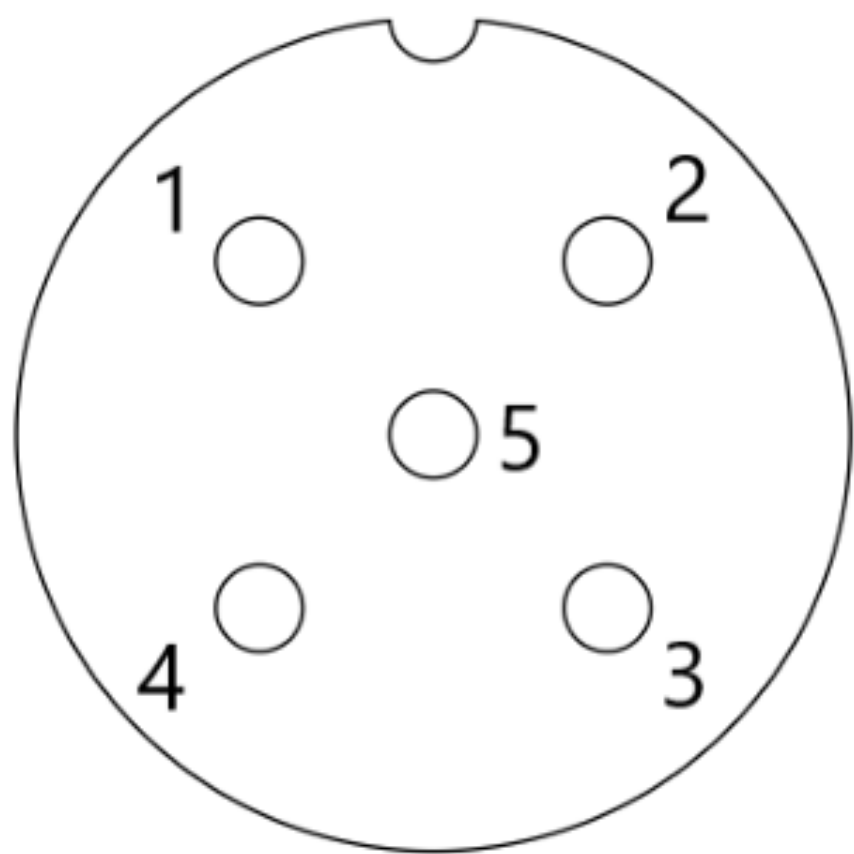


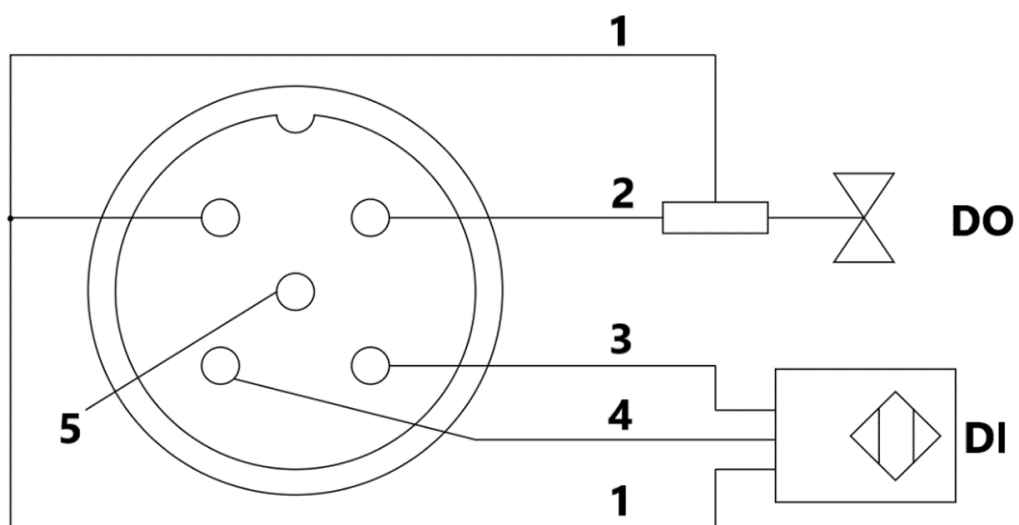


DI

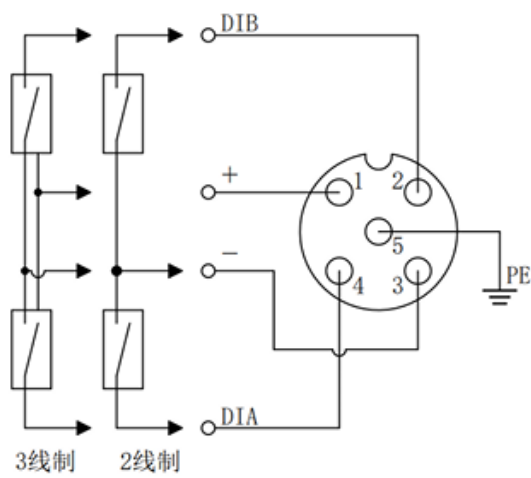


DO

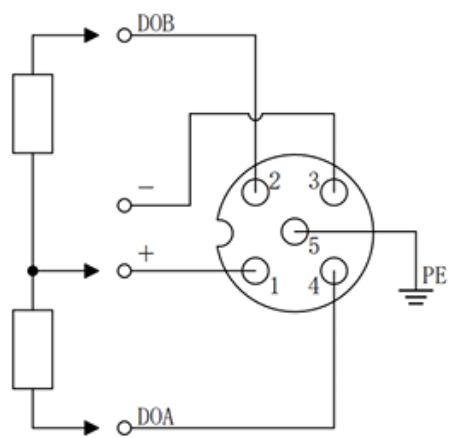


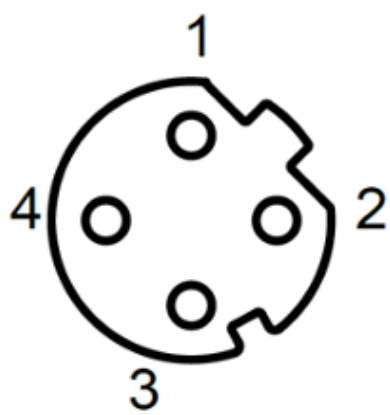


NPN输入



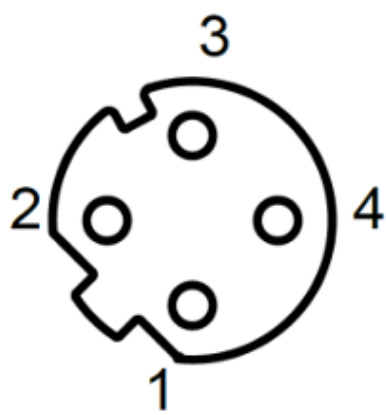
NPN输出





IN

Bus



OUT