

BR

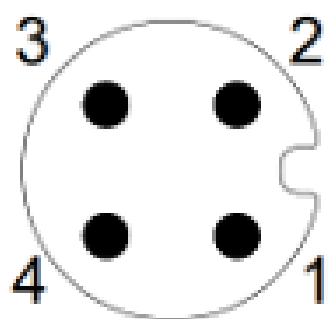


X1



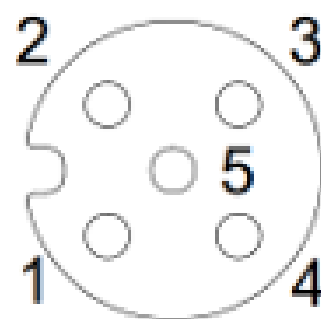
X10



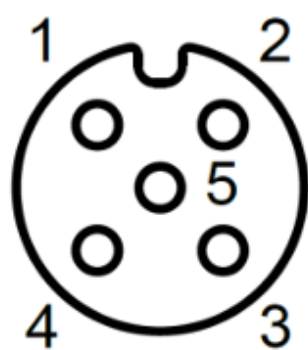


IN

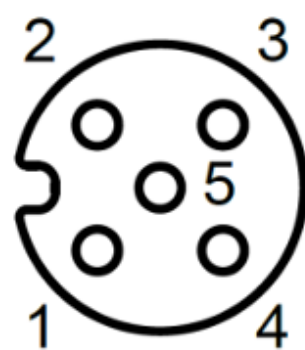
Bus



OUT

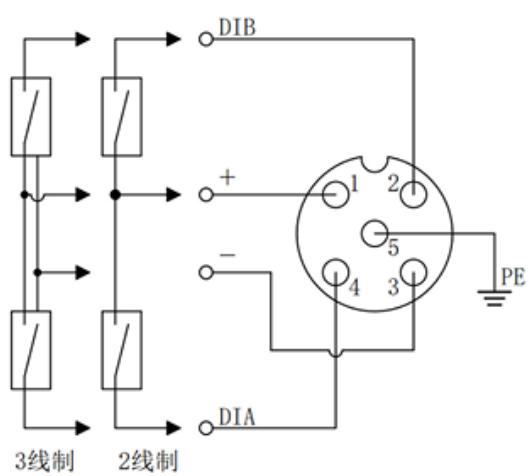


DI

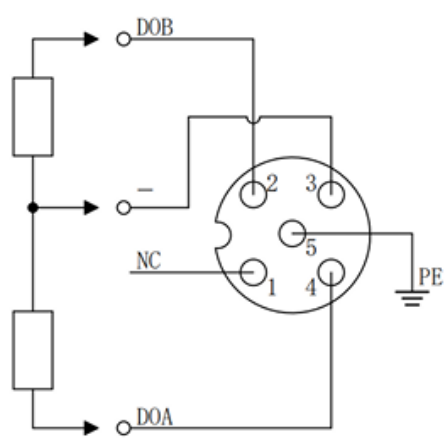


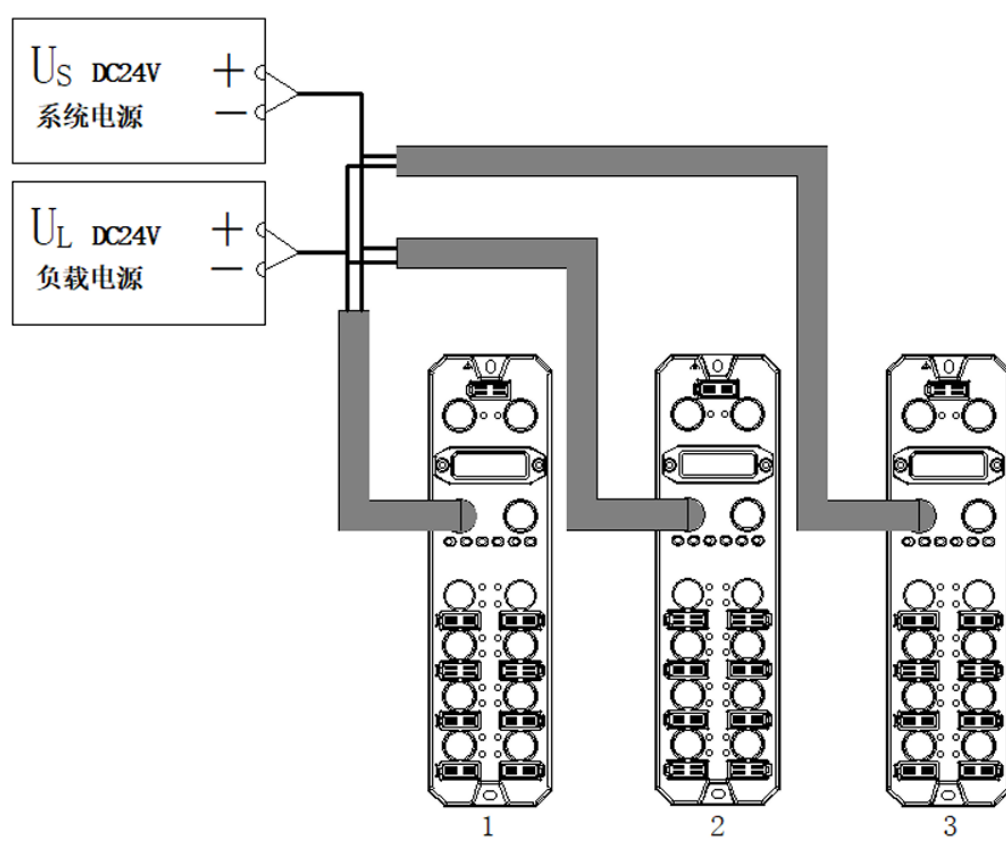
DO

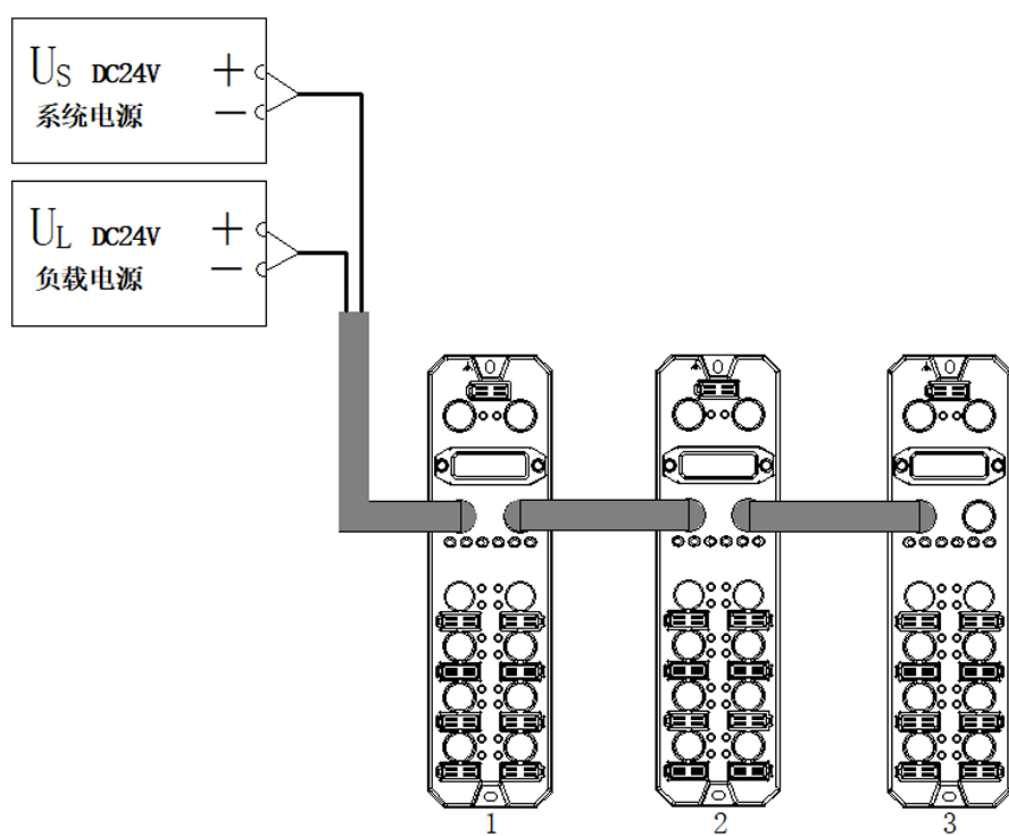
PNP输入

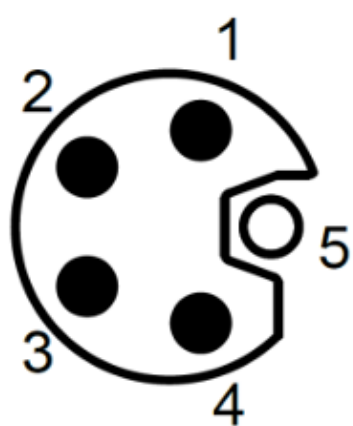


PNP输出

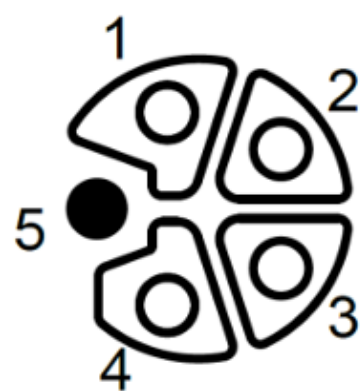








IN



Power

OUT

